

Team Name: sdmay24-12

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Report Period: Sept 11-Oct 24

Summary of Progress in this Period

In this period, we researched video compression and decompression algorithms. We researched possible development boards with FPGAs and HDMI ports. We clearly defined the software and hardware requirements, as well the technical standards and the demo requirements. Lastly, we created a project plan highlighting the key milestones and their corresponding tasks.

Pending Issues

Benchmark algorithms. Find applicable algorithms.

Plans for Upcoming Reporting Period

Our goal is to have a short list of ideal algorithms that we are considering. If possible, we also intend to meet with our faculty advisor and/or with our client.
